

AFC-800 AUTO FREE CALL

TECHNICAL SPECIFICATIONS

HARDWARE

Digital Signal Processors

- One TI TMS320VC549 DSP per module
- 100 MIPS per DSP processor
- On-chip memory: 32K word of SRAM, 16K word of ROM
- Local SRAM 128K x 16 for each DSP

Control Processor

- 32-bit ARM7 TDMI core
- 8K byte unified cache
- 4Kword Write buffer
- Embedded on-chip Ethernet MAC with associated BDMA
- Local 2M x 32 SDRAM and 2M x 16 Flash

I/O

- Standard 10/100 BaseTX RJ 45 interface
- RJ 11 Loop Start interfaces for FXS/FXO
- RJ 45 for E&M Type V 4w interface

Mechanical, Environment & Power

- 19" rack mountable or desk top
- Operating temperature: 32 to 122 F (0°C to 50°C)
- Operating humidity: 10% to 95% (non-condensing)
- Storage temperature: 14 to 140 F (-10 to 60°C)
- AC-to-DC power supply (90-260 VAC, auto-ranging, 50-60 Hz.)

Compliant

- CE
- FCC part 15 A
- FXS/FXO/E&M modules (Compliant with ITU-T G.712)
- UL

Line Module Specifications

	FXO	FXS	E&M
Signaling:	Loop Start / DTMF	Loop Start / DTMF	Type V, 4w (Wink, Delay, Immediate)
No. of Channels:	4	4	4
Interface Connectors:	4 RJ 11 2-pin modular jacks.	4 RJ 11 2-pin modular jacks.	4 RJ 45 8-pin modular jacks.
Line Impedance :	600 Ω 900 Ω Complex line impedance	600 Ω 900 Ω Complex line impedance	600 Ω
Insertion Loss:	2 dB nominal (Adjustable)	2 dB nominal (Adjustable)	2 dB nominal (Adjustable)
Frequency Response:	300Hz - 3400Hz +/- 2dB w.r.t. 1004Hz.	300Hz - 3400Hz +/- 2dB w.r.t. 1004Hz.	300Hz - 3400Hz +/- 2dB w.r.t. 1004Hz.
Return Loss:	≥ 18 dB	≥ 18 dB	≥ 18 dB
Input Level Adjustment:	-6 dB to +6 dB.	-6 dB to +6 dB.	-6 dB to +6 dB.
Output Attenuation:	0 dB to 13 dB.	0 dB to 13 dB.	0 dB to 13 dB.
Longitudinal Balance:	≥ 45 dB	≥ 45 dB	≥ 45 dB
Loop Current:	N/A	25mA nominal	N/A
Ring Voltage:	N/A	40Vrms Nominal	N/A
Ring Tone:	N/A	16.67Hz, 20Hz(default), 25Hz or 50Hz	N/A

AUTOMATIC ELECTRIC COMPANY
No.141,Sec.1,Huei-Lai Rd.,Western Dist.
TAICHUNG, 40756, TAIWAN, R.O.C.
<http://www.autoafc.com.tw>
TEL:886-4-22553311
E-mail:service@autoafc.com.tw

SOFTWARE

Speech

- Compression algorithms: ITU G. 711, G.723.1, and G.729A/B.
- Hybrid echo cancellation G.168 (16 ms)
- Auto switch between Fax and voice
- DTMF tone detection/regeneration
- Channel: four channels per module
- Comfort Noise Generation (CNG)
- User programmable Call Progress detection/generation
- Voice Activity Detection (VAD)
- User programmable Gain Control

Fax

- Facsimile protocol: T.30 Group 3
- Modulation formats: V.21, V.27ter, V.29, V.17
- Real-time fax over IP
- DTMF tone detection/regeneration

Management Tools

- RS 232 console port interface
- HTTP Server
- Telnet Server
- Elite Server for RAS and dial plan management
- TFTP and flash memory for remote software download and upgrade

H.323 Protocol Stack

- RAS sub-stack for Terminals and Gatekeepers: supports all mandatory and optional messages (Tx and Rx) as specified in table 19/H.255.0
- H.245 sub-stack: supports the Signaling Entities of Master Slave Determination, Capability Exchange, Open Logical Channels, and Close Logical Channels
- Q.931: supports all mandatory messages as specified in table 4/H.255.0
- Compliant with H.323 Version 1 and Version 2

ORDERING INFORMATION

Part Number	Description
AFC-804	AFC-804 supporting 4 voice/fax channels with FXO interface
AFC-804S	AFC-804S supporting 4 voice/fax channels with FXS interface
AFC-804E	AFC-804E supporting 4 voice/fax channels with E&M interface
AFC-808	AFC-808 supporting 8 voice/fax channels with FXO interface
AFC-808S	AFC-808S supporting 8 voice/fax channels with FXS interface
AFC-808E	AFC-808E supporting 8 voice/fax channels with E&M interface
AFC-FXO	4-port line module FXO interface
AFC-FXS	4-port line module FXS interface
AFC-ENM	4-port line module E&M interface
AFC-GK	AFC Server

Trademarks:

TMS320LC549 is a trademark of Texas Instruments.
 ARM7 us a trademark of ARM Ltd.